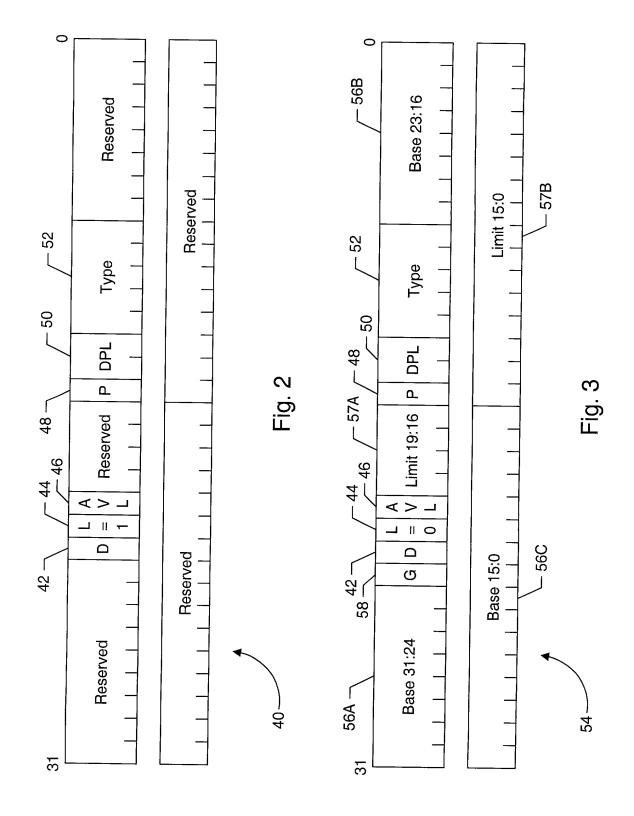


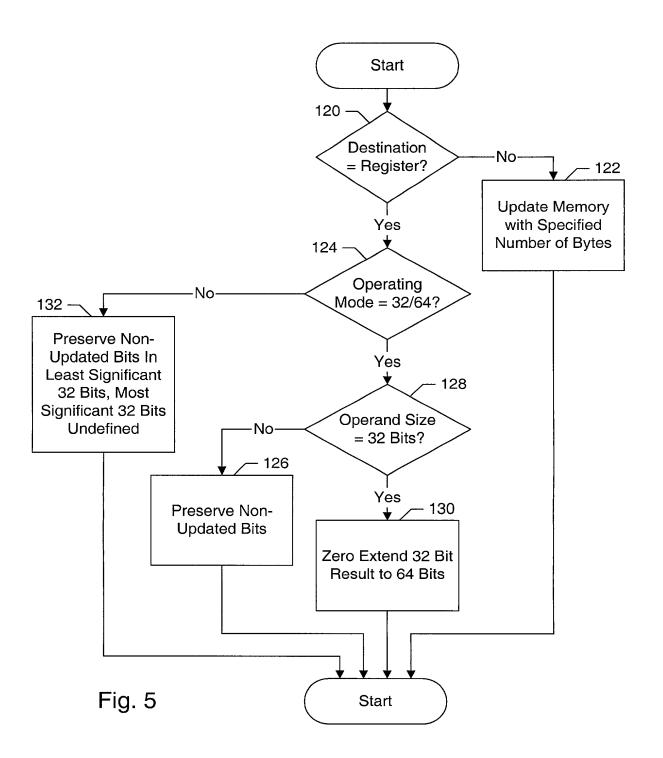
Fig. 1

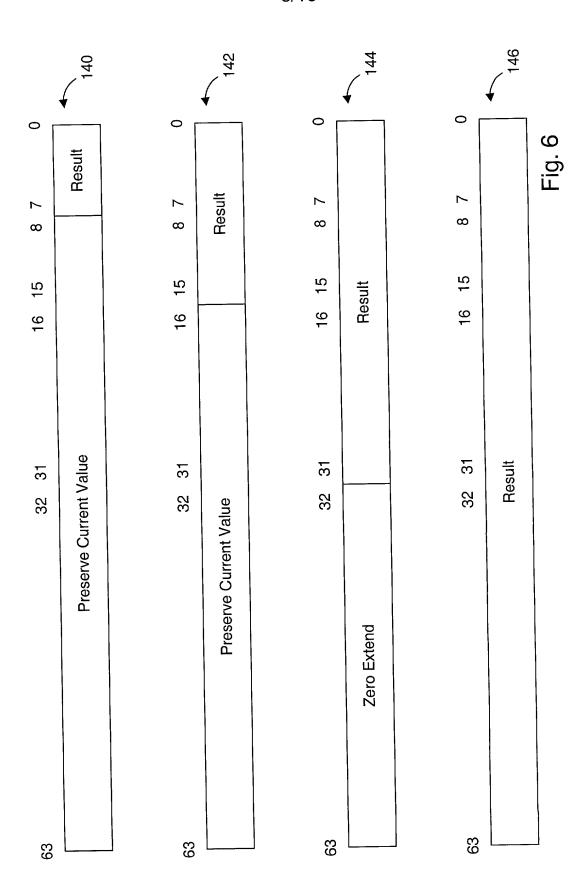


<u>LME</u>	CS L Bit	CS D Bit	Operating Mode
0	х	0	16 Bit Mode
0	х	1	32 Bit Mode
1	0	0	16 Bit Compatibility Mode
1	0	1	32 Bit Compatibility Mode
1	1	0	32/64 Mode
1	1	1	Reserved



Fig. 4





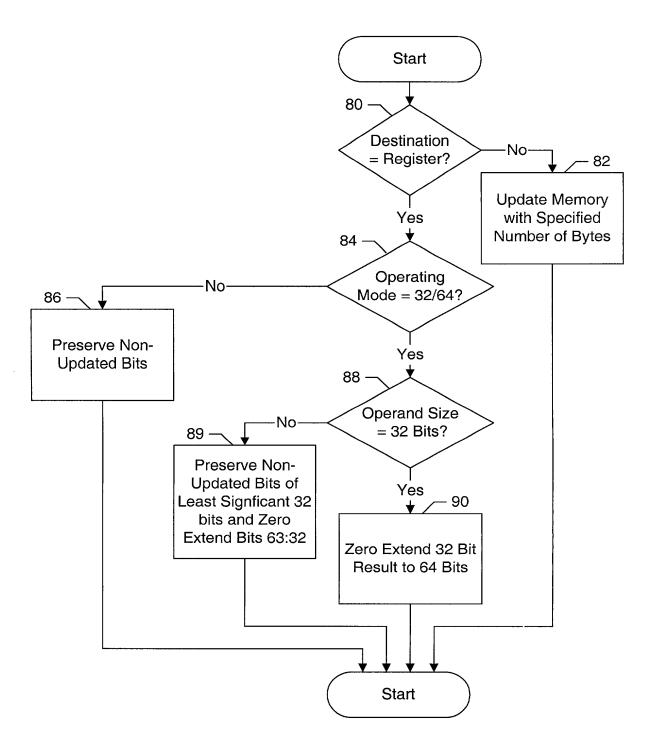
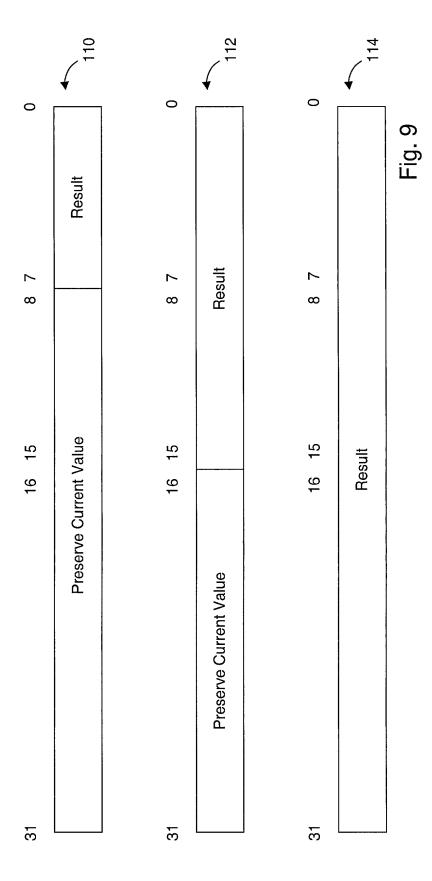
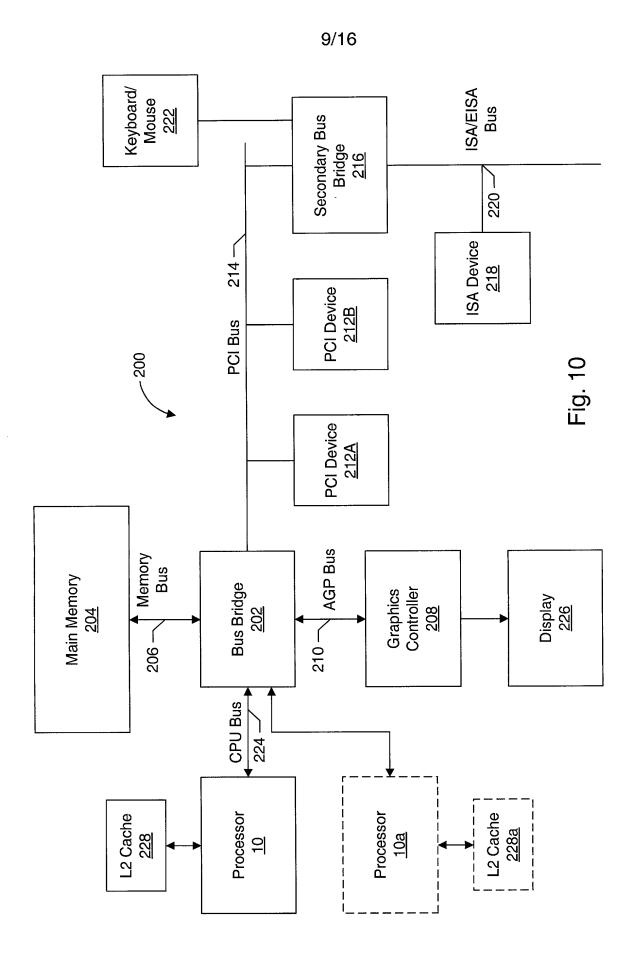


Fig. 7

63	32	31	16 15	8 7	0
	Zero Extend	Preserve Current Value	ent Value	Result	√ 100
					ı
63	32	31	16 15	8 7	0
	Zero Extend	Preserve Current Value	en	Result	102
					1
63	32	2 31	16 15	8 7	0 1
	Zero Extend		Result		₹ 104
63	32	2 31	16 15	8 7	0 1
	ш	Result			106
				Fig. 8	1





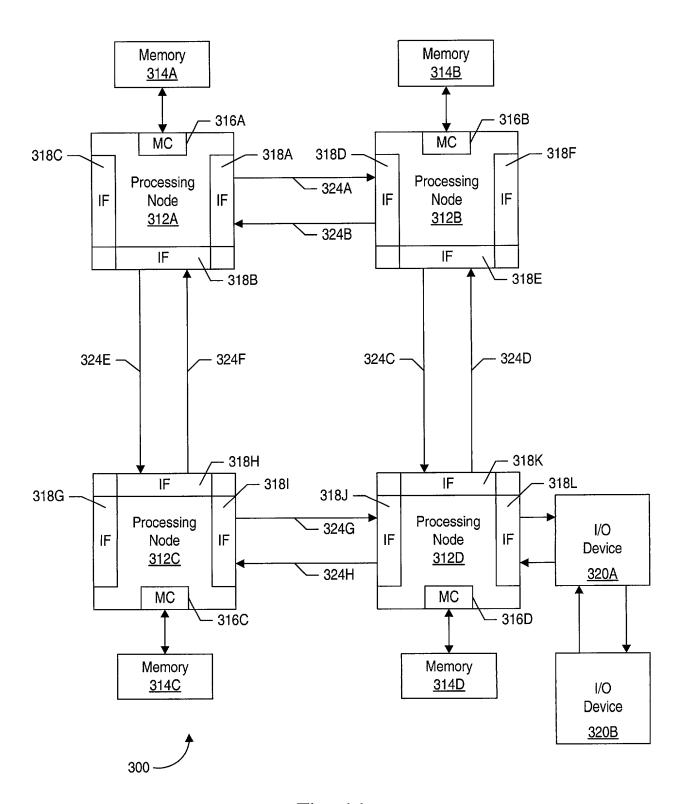


Fig. 11

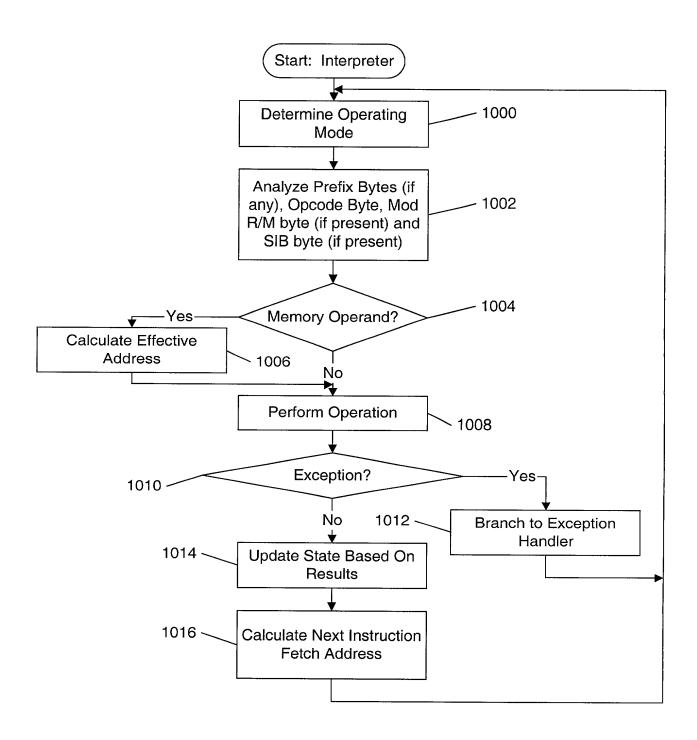


Fig. 12

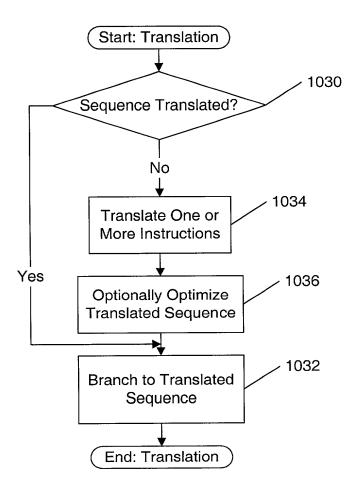


Fig. 13

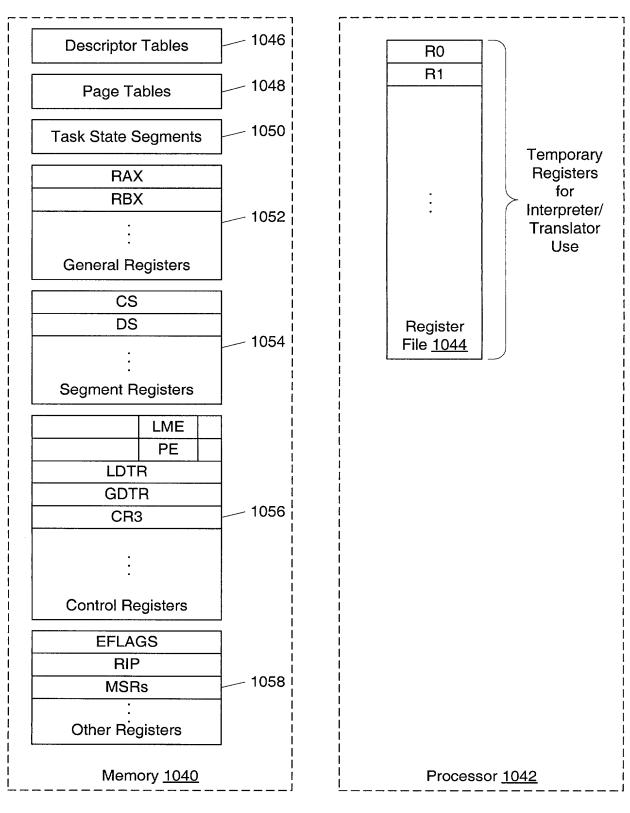


Fig. 14

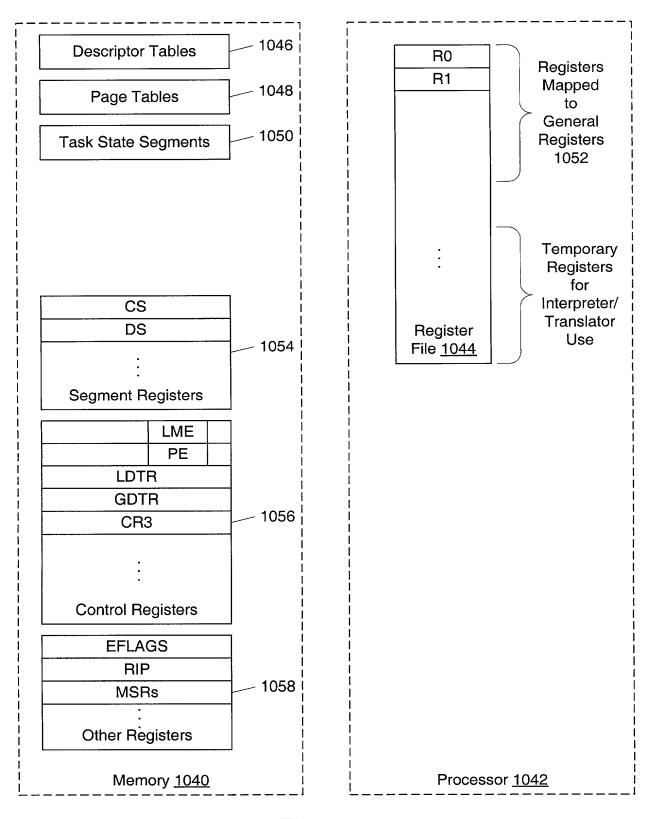


Fig. 15

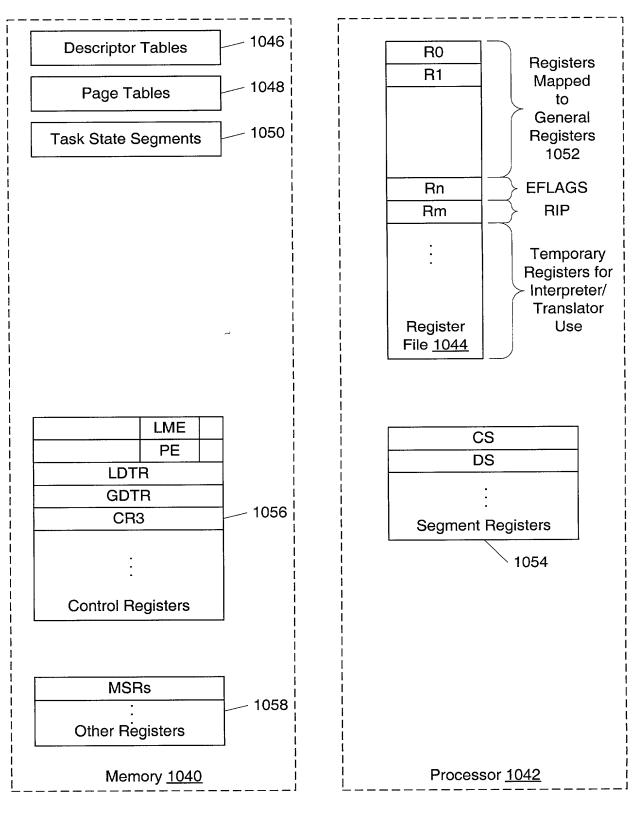


Fig. 16

16/16

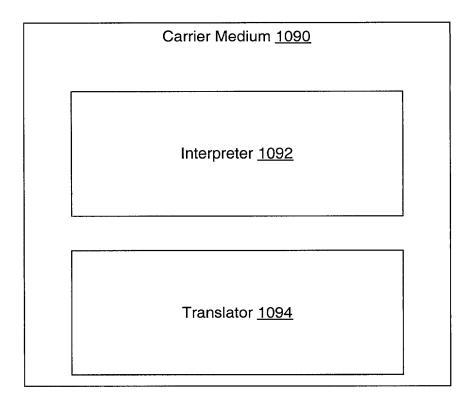


Fig. 17